## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Ashjaee et al.

Group Art Unit: Not yet assigned
Examiner: Not yet assigned

Serial No.: Not yet assigned Docket: NT-261C1-US

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Title: Method Of Sealing Wafer Backside For

Full-Face Processing

# **INFORMATION DISCLOSURE STATEMENT**

#### **US PATENT DOCUMENTS**

| Examiner  | Cite | Document Number | Publication Date | Name of Patentee or   |                          |
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Examiner Signature Date Considered 02/25/05.

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